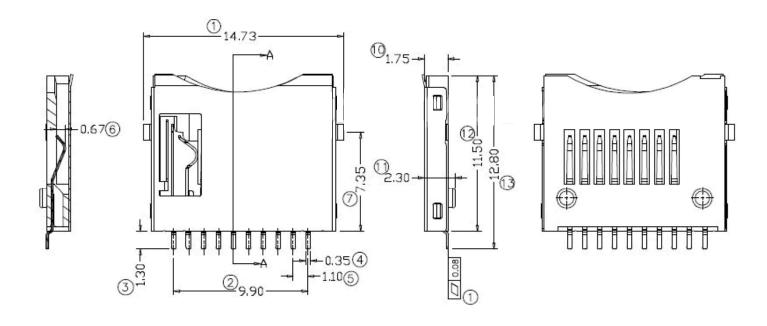
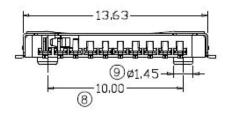
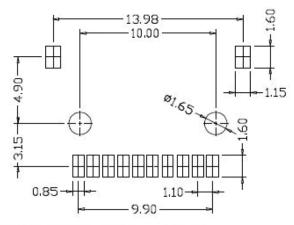
## 









RECOMMENDED PCB LAYOUT TOP VIFW (.XX±0.05)